



## UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE  
 United States Patent and Trademark Office  
 Address: COMMISSIONER FOR PATENTS  
 P.O. Box 1450  
 Alexandria, Virginia 22313-1450  
[www.uspto.gov](http://www.uspto.gov)



Bib Data Sheet

CONFIRMATION NO. 4264

SERIAL NUMBER 10/664,884	FILING DATE 09/22/2003  RULE	CLASS 331	GROUP ART UNIT 2817	ATTORNEY DOCKET NO. Q77404
-----------------------------	---------------------------------------	--------------	------------------------	----------------------------------

## APPLICANTS

Keiji Hayashida, Kawasaki, JAPAN;

Atsushi Hasegawa, Kawasaki, JAPAN;

\*\* CONTINUING DATA \*\*\*\*\* *LK nov*\*\* FOREIGN APPLICATIONS \*\*\*\*\* *LK*

JAPAN 2002-281842 09/26/2002

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 12/11/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY JAPAN	SHEETS DRAWING 6	TOTAL CLAIMS 11	INDEPENDENT CLAIMS 2
Verified and Acknowledged _____ Examiner's Signature	Initials				

## ADDRESS

23373  
 SUGHRUE MION, PLLC  
 2100 PENNSYLVANIA AVENUE, N.W.  
 SUITE 800  
 WASHINGTON , DC  
 20037

## TITLE

Phase-locked loop circuit reducing steady state phase error

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time )
RECEIVED		